

Devi Ahilya Vishwavidyalaya, Indore
Institute of Engineering & Technology

CLASS TIME TABLE (Session: July- Dec 2025) w.e.f. 01/07/2025

Class & Branch: IV Year BE Electronics & Telecommunication (Section-B)						Class Teacher: Dr Shailendra Kumar Pathak			
Day	8:00-9:00	9:00-10:00	10:00-11:00	11:00-12:00	12:00-13:00	13:00-14:00	14:00-15:00	15:00-16:00	16:00-17:00
Monday		RMC* 7ETRE1 PS E-304	7ETRE1 PS B2 D-206		B R E A K	7ETRC2 PDA E-304	7ETRE1 PS E-304	7ETRC1 RY E-304	7ETRP1 RG
Tuesday		RMC* 7ETRC2 PDA E-304	7ETRE1 PS B1 D-206			7ETRC2 PDA E-304	7ETRC3 SKP E-304	7ETRC3 SKP B1 D204	
			7ETRC2 PDA B2 D102						
Wednesday		RMC* 7ETRC3 SKP E-304	7ETRC1 RY E-304	7ETRE1 PS E-304		7ETRC2 PDA E-304	7ETRC3 SKP E-304	7ETRC2 PDA B1 D102	
								7ETRC3 SKP B2 D204	
Thursday		RMC* 7ETRC1 RY E-304	7ETRC3 SKP E-304	7ETRE1 PS E-304		7ETRC1 RY E-304	7ETRC2 PDA E-304	7ETRP1 RG	7ETRP1 RG
Friday		7ETRP1 RG	7ETRC3 SKP E-304	7ETRE1 PS E-304		7ETRC1 RY E-304	7ETRP1 RG	7ETRP1 RG	7ETRP1 RG
Saturday									
Subject Code	Subject Name		Marks Scheme		Keys.	Faculty Name		Keys.	Faculty Name
7ETRC1	Operating System		L-3, T-1, P-0		RY	Mr Ravindra Yadav			
7ETRC2	Antenna and Wave Propagation		L-3, T-1, P-1		PDA	Dr P D Ashok			
7ETRC3	RF and Microwave Engg		L-3, T-1, P-1		SKP	Dr Shailendra Kumar Pathak			
7ETRE1	SoC Design and Verification		L-3, T-1, P-1		PS	Ms Priyanka Sharma			
7ETRP1	Project Phase - I		L-0, T-0, P-7		RG	Respective Guide			

Note:- Classes will be conducted only in allotted rooms & time, Mutual exchange of lectures is not permitted until the permission of Co-ordinator /HOD/Director

RMC* Remedial classes for weaker and underperformed student

Prof. I/C

HOD

Director