

Institute of Engineering And Technology, DAVV

1 YR M. TECH. Microelectronics & VLSI Design Nov -Dec 2024

Sr No.	Roll Number	Enrollment Number	Student Name	ADVANCE DIGITAL SYSTEM DESIGN	EMBEDDED SYSTEM	CMOS DIGITAL VLSI DESIGN	INTRODUCTION TO SCRIPTING LANGUAGE	MOS PHYSICS AND VLSI TECHNOLOGY	FPGA SYSTEM DESIGN USING VERILOG	CMOS DIGITAL VLSI DESIGN USING CADENCE EDA TOOL	COMPREHENSIVE VIVA -I	Result	SGPA
				1MVRC1	1MVRC2	1MVRC3	1MVRE1	1MVRG1	1MVRL1	1MVRL2	1MVRV1		
				3	3	3	2	2	3	2	4		
				Th	Th	Th	Th	Th	Pr	Pr	Pr		
1	24MV1101	DE24886	ABHISHEK DHAKAD	P	B+	B+	A+	C	B+	A+	A	Pass	6.95
2	24MV1103	DE24888	ASHISH RAJVAIDYA	C	B	C	B+	C	B+	A+	A+	Pass	6.68
3	24MV1104	DE21624	AYUSH LONARE	P	C	B+	P	P	B+	A	A	Pass	6.05
4	24MV1105	DE24889	CHINMAY DUBEY	B	O	A	A+	A	A+	B+	A+	Pass	8.32
5	24MV1106	DE21635	DHEERAJ	B+	B	B+	A+	B	A	A	A+	Pass	7.55
6	24MV1107	DE24890	HARSHA VERMA	A	A	B+	A	B+	A+	B+	A+	Pass	8
7	24MV1108	DE24891	ISHAAN AYACHIT	A	B+	A	A	A	A	B+	A	Pass	7.77
8	24MV1109	DE24892	JAY PAL	B	A	B+	A+	A	B+	B+	B+	Pass	7.27
9	24MV1110	DE24893	PRASHANT KAUSHAL	P	A	B	P	B	A	B+	B+	Pass	6.36
10	24MV1111	DE24894	SHUBHAM KUMAR	B+	A	B	A	A+	A	A	A	Pass	7.68
11	24MV1112	DE24895	SURAJ BAGHEL	B+	A	B+	A	A+	A+	B+	A	Pass	7.86
12	24MV1113	DE24896	VAIBHAV RAWAT	P	C	B	B	P	B+	B+	A	Pass	6
13	24MV1114	DE24897	VIJAY LASHKARI	C	A	A	C	B	A	A	A	Pass	7.14
14	24MV1115	DE24898	VIKAS YADAV	B	B	A	A	A+	A+	B+	A+	Pass	7.77
15	24MV1116	DE20283	YOGESH CHOUHAN	P	P	B	A	B	B+	B+	A	Pass	6.23
No. of Failed Students				0	0	0	0	0	0	0	0		

Category	Number of Students
Pass	15
Fail	0
Not Registered	0