Devi Ahilya University, Indore, India Institute of Engineering & Technology				**II Year B.E. (Electronics and Instrumentation Engg.)**			
Subject Code & Name	**Instructions Hours per Week**			**Credits**			
3EIRG1	**L**	**T**	**P**	**L**	**T**	**P**	**Total**
NETWORK ANALYSIS	**3**	**1**	**0**	**3**	**1**	**0**	**4**
Duration of Theory Paper: 3 Hours							

Learning Objectives:

- To learn techniques of solving circuits involving different active and passive elements.
- To analyze the behavior of the circuit's response in time domain.
- To analyze the behavior of the circuit's response in frequency domain.
- To understand the significance of network functions.

Pre requisites: Basic course in Electrical Engg

COURSE CONTENTS

UNIT-I

Circuit fundamental and tools of analyzing Network: Elements, Sources, their characteristics, source transformations, Kirchhoff's law, node and loop analysis, Δ-Y transformation, Network Theorems: Superposition, Reciprocity, Thevenin's, Norton's , Millman and Maximum Power Transfer Theorem applied to AC circuit, Coupled circuits, Network topology, Concept of Network graph, Tree, tree branches and links, cut set and tie set schedules.

UNIT-II

Time domain analysis of circuits: Transient and steady state analysis of first order and second order systems, initial and final conditions in networks, Solution of I and II order differential equation model of RLC circuits with DC and AC source.

UNIT-III

Two Port Networks: Various network parameters, two port parameters – z-parameter, y-parameter, transmission parameter and hybrid parameter, relationships, Interconnection of two or more -two port networks, terminated two port network.

UNIT-IV

Laplace Transforms and Fourier analysis: Basic theorems for Laplace transform, solution of circuit problems using Laplace transform, Theorem in transform domain, Fourier analysis of complex waves, symmetries, introduction to Fourier transforms.

UNIT-V

Network Synthesis: Network functions, significance of poles and zeros, Positive real condition, Hurwitz and Routh test for stability of LTI Network. Synthesis of one port network, properties of LC immittances, foster realization of LC circuits, ladder development and Cauer forms, properties of RC immittances and synthesis of RC circuits.

Learning Outcomes:

Upon Completing the Course, Student will able to:

- Understand behavior of different circuits and their response using various circuit analysis tools and theorems
- Understand the analysis in time domain and frequency domain.
- Understand basic concepts regarding the system definition mathematically and associated network function.
- Understand the concept of Network synthesis.

BOOKS RECOMMENDED:

[1] M.E.VanValkenburg, Network Analysis, 3/e, Pearson Education.

[2] Franklin F.Kuo, Network Analysis and Synthesis, 2/e, John Wiley & Sons, 2003

[3] Donald Scott, An Introduction to Circuit Analysis: A System Approaches, Electrical Engineering Series, McGraw-Hill International Editions, 1987

[4] T.S.K.V. Iyer, Theory and Problem in Circuit Analysis, TMH Outline Series, Tata McGraw- Hill Publishing Company Ltd, New Delhi, 2000